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APPLICATION NO. FILING DATE		ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/615,601 07/07/2003		07/07/2003	Abhishek Lal	852463.402	8291	
500	7590 10/01/2004			EXAMINER		
		UAL PROPERTY	JEANGLAUDE, JEAN BRUNER			
701 FIFTH A SUITE 6300			· ART UNIT	PAPER NUMBER		
SEATTLE,	WA 981	04-7092	2819			
				DATE MAILED: 10/01/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Applicat	ion No.	Applicant(s)				
		10/615,6	601	LAL, ABHISHEK				
	Office Action Summary	Examine	er	Art Unit	, ,,,			
		Jean B J	eanglaude	2819				
Period fo	The MAILING DATE of this commun or Reply	nication appears on th	ne cover sheet with the	correspondence address				
THE - External after - If the If No	MAILING DATE OF THIS COMMUN ensions of time may be available under the provision of SIX (6) MONTHS from the mailing date of this come e period for reply specified above is less than thirty (6) o period for reply is specified above, the maximum soure to reply within the set or extended period for reply reply received by the Office later than three months ned patent term adjustment. See 37 CFR 1.704(b).	IICATION. s of 37 CFR 1.136(a). In no e munication. 30) days, a reply within the sta tatutory period will apply and o y will, by statute, cause the ap	vent, however, may a reply be attory minimum of thirty (30) diwill expire SIX (6) MONTHS frouplication to become ABANDON	timely filed ays will be considered timely, m the mailing date of this communicat IED (35 U.S.C. § 133).	ion.			
Status								
1)⊠	Responsive to communication(s) fil	ed on 07 July 2003.						
2a)□		2b)⊠ This action is	non-final.					
3)[	Since this application is in condition	for allowance excep	t for formal matters, p	rosecution as to the merits	is			
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposit	ion of Claims							
. 4)⊠	Claim(s) 1-14 is/are pending in the	application.						
	4a) Of the above claim(s) is/a	are withdrawn from co	onsideration.					
5)🖂	Claim(s) 5-14 is/are allowed.							
6)⊠	Claim(s) <u>1-4</u> is/are rejected.							
7)[	Claim(s) is/are objected to.							
8)□	Claim(s) are subject to restri	ction and/or election	requirement.					
Applicat	ion Papers	•						
9)[	The specification is objected to by the	ne Examiner.						
10)⊠	The drawing(s) filed on 07 July 2003	3 is/are: a)⊠ accept	ed or b)□ objected to	by the Examiner.				
	Applicant may not request that any obje	ection to the drawing(s)	be held in abeyance. S	ee 37 CFR 1.85(a).				
	Replacement drawing sheet(s) includin	g the correction is requi	ired if the drawing(s) is o	bjected to. See 37 CFR 1.121	l(d).			
11)	The oath or declaration is objected t	o by the Examiner. N	lote the attached Offic	e Action or form PTO-152.				
Priority (	under 35 U.S.C. § 119				•			
	Acknowledgment is made of a claim  ☐ All b)☐ Some * c)☐ None of:  1.☐ Certified copies of the priority	documents have be	en received.					
	<ul><li>2. Certified copies of the priority</li><li>3. Copies of the certified copies application from the Internation</li></ul>	of the priority docum	ents have been recei					
* (	See the attached detailed Office action	·	, ,,	ved.				
A440.ab	.4(5)							
Attachmer  1) Notice	nt(s) ce of References Cited (PTO-892)		4) Interview Summai	n/(PT∩_413\				
	ce of Draftsperson's Patent Drawing Review (	PTO-948)	Paper No(s)/Mail I					
3) 🔲 Infor	mation Disclosure Statement(s) (PTO-1449 or er No(s)/Mail Date		5) Notice of Informal 6) Other:	Patent Application (PTO-152)				

#### **Detailed Action**

## Claim Objection

1. Claim 1 is objected to because of the following informalities: it is suggested to substitute "a" before the word "input" in claim 1, line 1 by – an --. Appropriate correction is required.

## Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1, 3, 4 are rejected under 35 U.S.C. 102(b) as being anticipated by Sato (US patent Number 4,710,960).
- 4. Regarding claims 1, and 4. Sato discloses an improved binary decoder and method (figs. 2, 3) that comprise a selection means [multiplexer] (6, fig. 2) for activating a selected output corresponding to an input binary value [as noted in fig. 2 the multiplexed 6 has as inputs X2, X4 that bits sequence fed to the multiplexer 6 and the multiplexer 6 provides as output X5 a bit sequence 1 (col 4, lines 3 – 10) and deselecting means [demultiplexer] (11, fig. 3) coupled to each output that deactivates all other outputs when the selected output is activated [as noted in figs. 2, 3, the demultiplexer 11 is connected to the multiplexer 3 that deactivates the output of the multiplexer 3. When activating one of the outputs of the multiplexer 3, the other outputs turn off (deactivate) (col 4, lines 35 - 40).

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5. Regarding claim 3, Sato discloses an improved binary decoder (figs. 2, 3) wherein the deselecting means comprises a circuit arrangement having a single input connected to the selected output of the selection means and a plurality of outputs each of which is connected to one of the remaining outputs of the selection means such that when the input of the circuit arrangement is activate all the other output of the decoder forced to the inactive state [as noted in figs. 2, 3, the demultiplexer 11 is connected to the multiplexer 3 that deactivates the output of the multiplexer 3. When activating one of the outputs of the multiplexer 3, the other outputs turn off (deactivate) (col 4, lines 35 – 40).

# Claim Rejections - 35 USC § 103

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Sato (US patent Number 4,710,960) in view of the Applicant's admitted prior art (APA)
- 8. Regarding claim 2, Sato discloses all the limitations as discussed above except an improved binary decoder wherein the selection means comprises a circuit arrangement of gates for selecting output. However, the Applicant's admitted prior art discloses a binary decoder (figs. 1 4) wherein the selection means (transistors) comprising of gates to select output (figs. 1 4). It would have been obvious to one of ordinary skill in the art at the time the invention was made to couple the applicant 's

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admitted prior art to Sato's system in order to produce digital output from a selected input binary value.

## Allowable Subject Matter

9. Claims 5 – 14 are allowable.

10. Reasons for allowing the aforementioned claims will be provided in the next office action.

#### Conclusion

- 11. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 12. Bagley et al. (US Patent Number 3,564,510) discloses a selection, distribution and display system.
- 13. Brown et al. (US patent Number 4,027,152) discloses an apparatus and method for transmitting binary-coded information.
- 14. Moates (US patent Number 4,374,384) discloses a matrix encoder for resistive sensor arrays.
- 15. Breslow (US patent Number 4,443,788) discloses an optical encode system.
- 16. Miki et al. (US patent Number 4,958,157) discloses an encoder circuit with series connected output switching transistors.
- 17. Ishibashi et al. (US Patent Number 5,008,669) discloses an encoding/decoding system for optical recording/reading.
- 18. Nozuyama (US Patent Number 5,504,755) discloses a testable programmable logic array.

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19. Van Valburg et al. (US Patent Number 5,557,275) discloses an error tolerant binary encoder.

- 20. Miyanishi (US Patent Number 5,629,697) discloses a code conversion circuit.
- 21. Butts et al. (US patent Number 6,539,535) discloses a programmable logic device having integrated probing structures.
- 22. Pascucci (US Patent Number 6,696,990) discloses a binary encoding circuit.
- 23. Lee (US application Number 10/463,318) discloses a high-speed encoder for high speed ADC.
- 24. Kitayama et al. (JP410164010A) discloses an optical code division multiplex transmission access system and device.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean B Jeanglaude whose telephone number is 571-272-1804. The examiner can normally be reached on Monday - Friday 7:30 A. M. - 5:00

P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Han Bruner glandlande Jean Bruner Jeanglaude

Primary Examiner September 24, 2004